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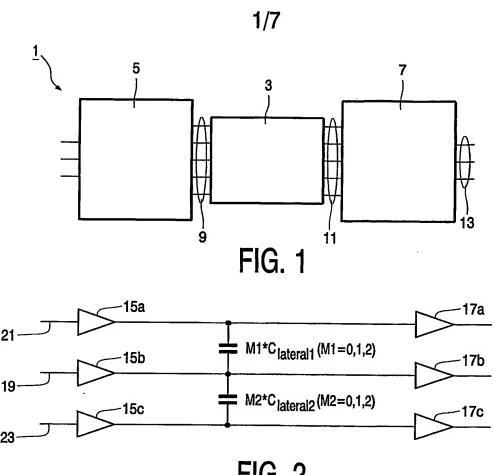
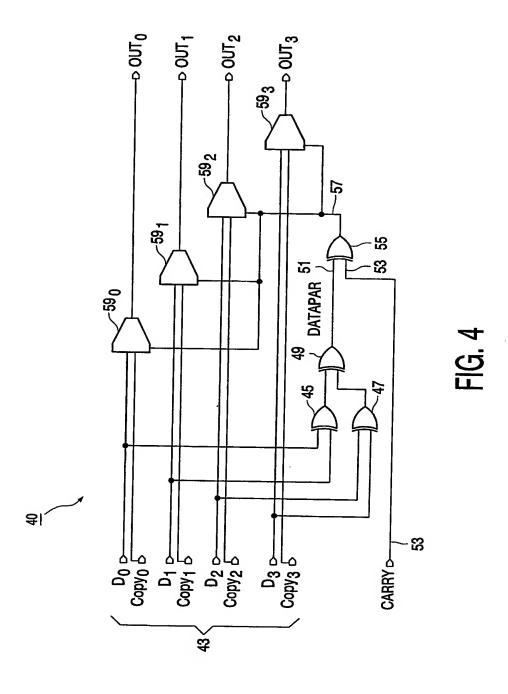


FIG. 2

Mode	Aggressor1	Victim	Aggressor2	Relative power (proportional to switched total capacitance)
Fastest (speed-up)	0 →1	0 → 1	0 → 1	C <sub>vertical'</sub> (including fringe)
Faster	0 → 1 Quiet	0 → 1 0 → 1	Quiet 0 <del></del> 1	C <sub>vertical</sub> " + 1* C <sub>lateral</sub>
Nominal	0 → 1 Quiet 1 → 0	0 → 1 0 → 1 0 → 1	1 → 0 Quiet 0 → 1	C <sub>vertical</sub> + 2* C <sub>lateral</sub>
Slower	1 → 0 Quiet	0 → 1 0 → 1	Quiet 1 → 0	C <sub>vertical<sup>mu</sup> + 3* C lateral</sub>
Slowest (slow-down)	1 → 0		1 → 0	C <sub>vertical</sub> + 4* C lateral

FIG. 3



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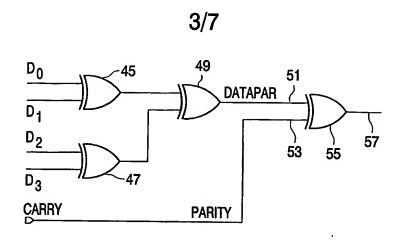


FIG. 5

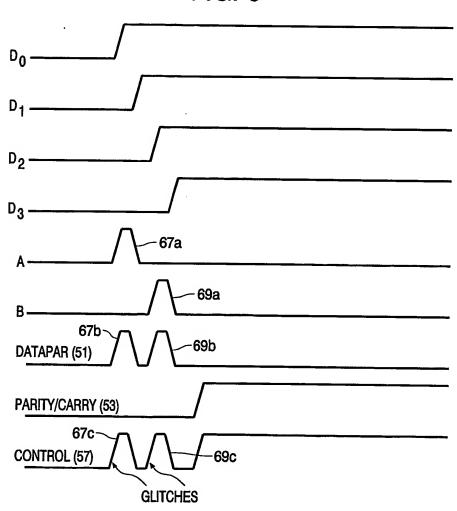
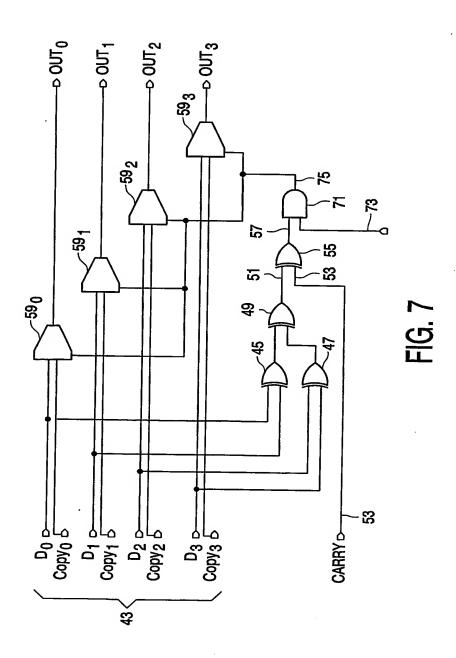


FIG. 6



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